Abstracts

A 4.5-GHz GaAs Dual-Modulus Prescaler IC (Short Papers)

M. Ohhata, T. Takada, M. Ino, N. Kato and M. Ida. "A 4.5-GHz GaAs Dual-Modulus Prescaler IC (Short Papers)." 1988 Transactions on Microwave Theory and Techniques 36.1 (Jan. 1988 [T-MTT]): 158-160.

A 4.5-GHz 100-mW GaAs divide-by-256/258 dual-modulus prescaler with a reset function has been developed. The operating frequency obtained for this modulus prescaler is the highest to date, while the power dissipation is comparable to others that have been reported. The supply voltage is as low as 3 V. A low-power, source coupled FET logic (LSCFL) using novel level shift circuits and 0.5-µm-gate buried P-Layer SAINT (BP-SAINT) FET's have been used to achieve this high performance.

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